

FIG. 1

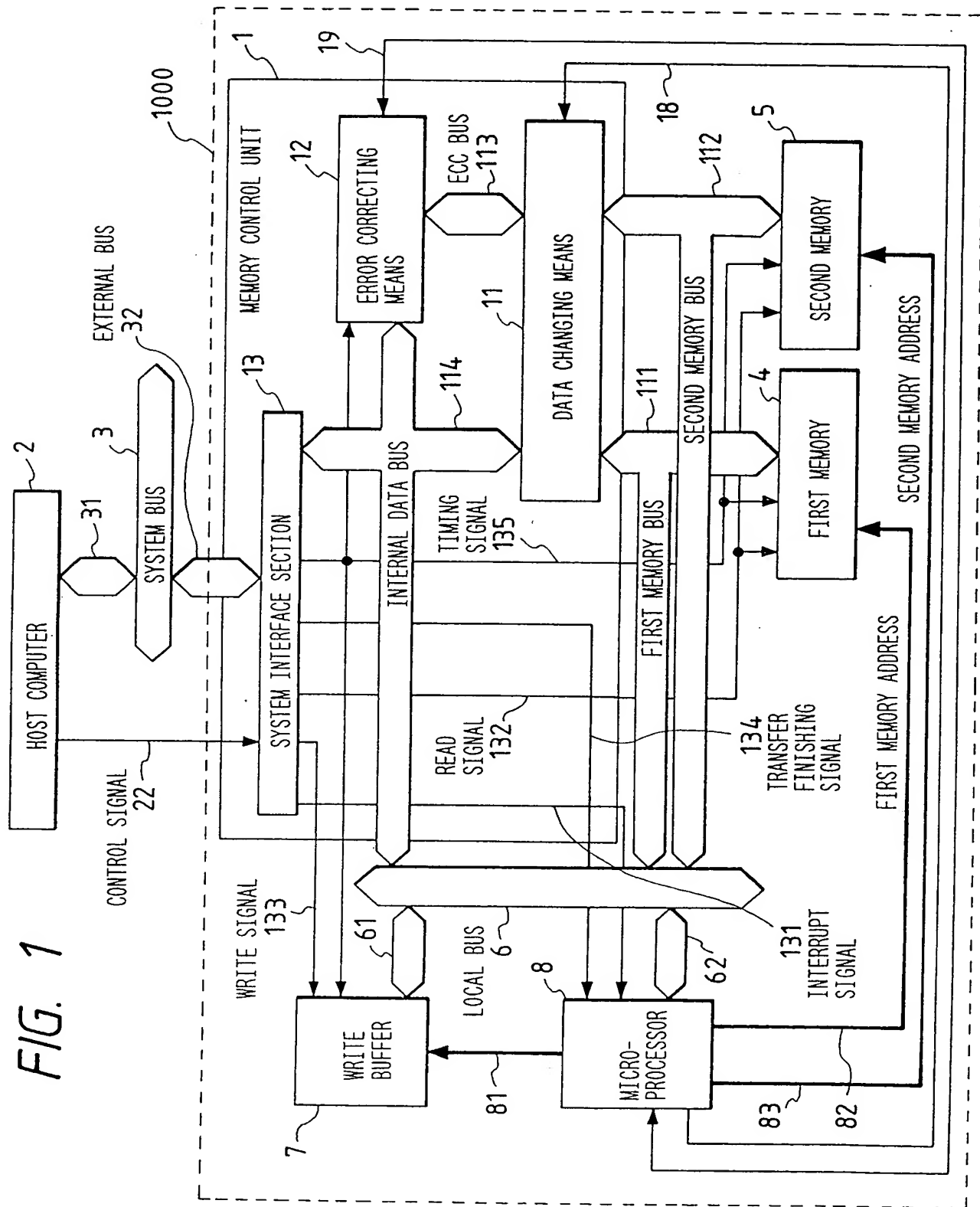


FIG. 2

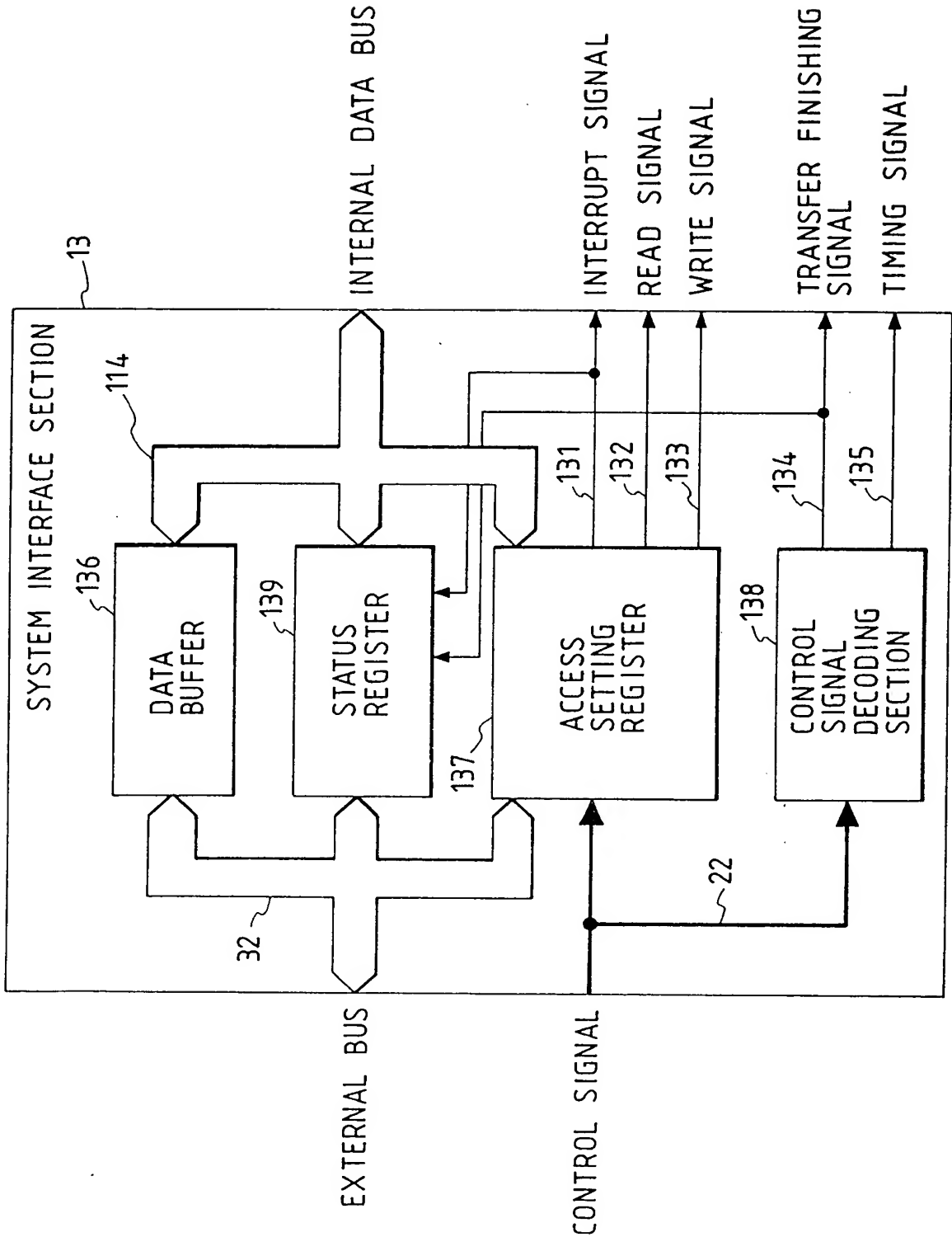


FIG. 3

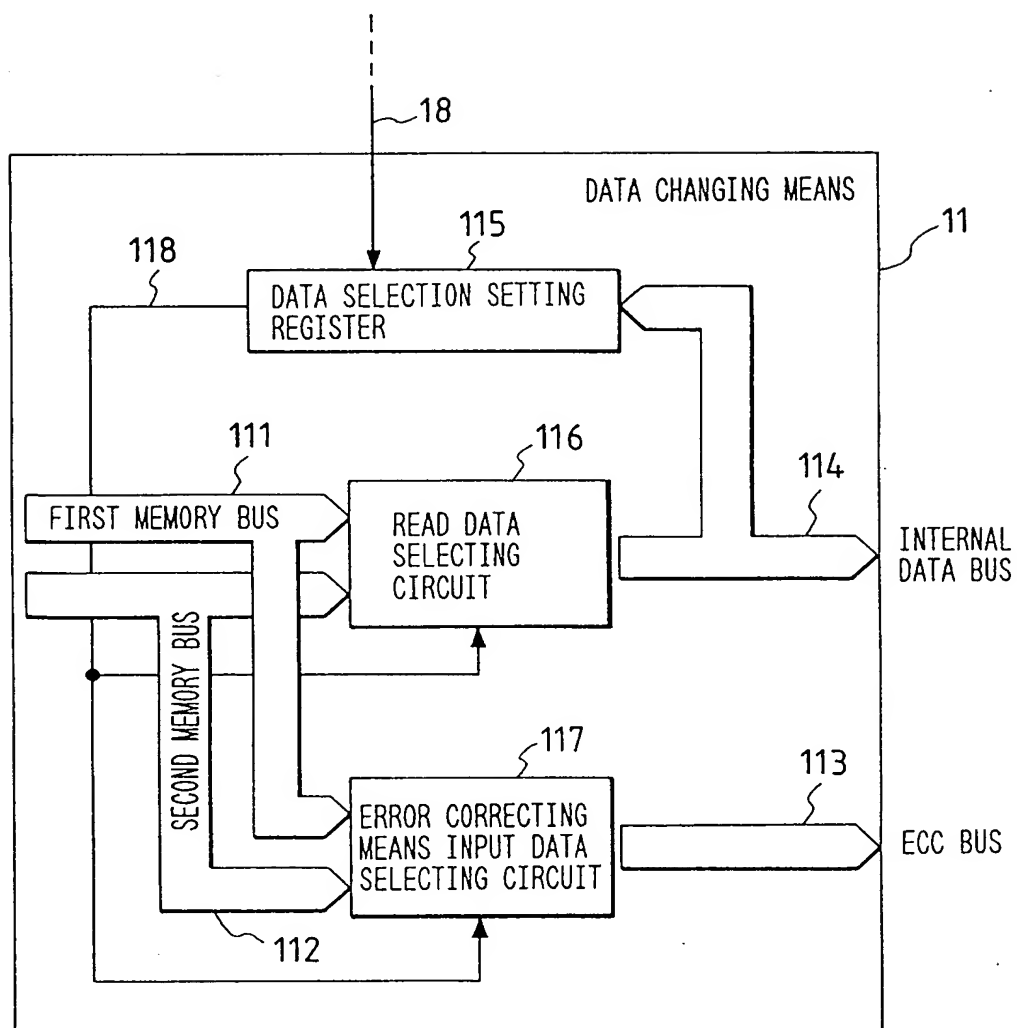


FIG. 4

(SELECTING CIRCUIT (116))

DATA SELECTION SETTING REGISTER 115	INTERNAL DATA BUS 114
0	FIRST MEMORY BUS 111
1	SECOND MEMORY BUS 112

FIG. 5

(SELECTING CIRCUIT (117))

DATA SELECTION SETTING REGISTER 115	EEC BUS 113
0	FIRST MEMORY BUS 112
1	SECOND MEMORY BUS 111

FIG. 6

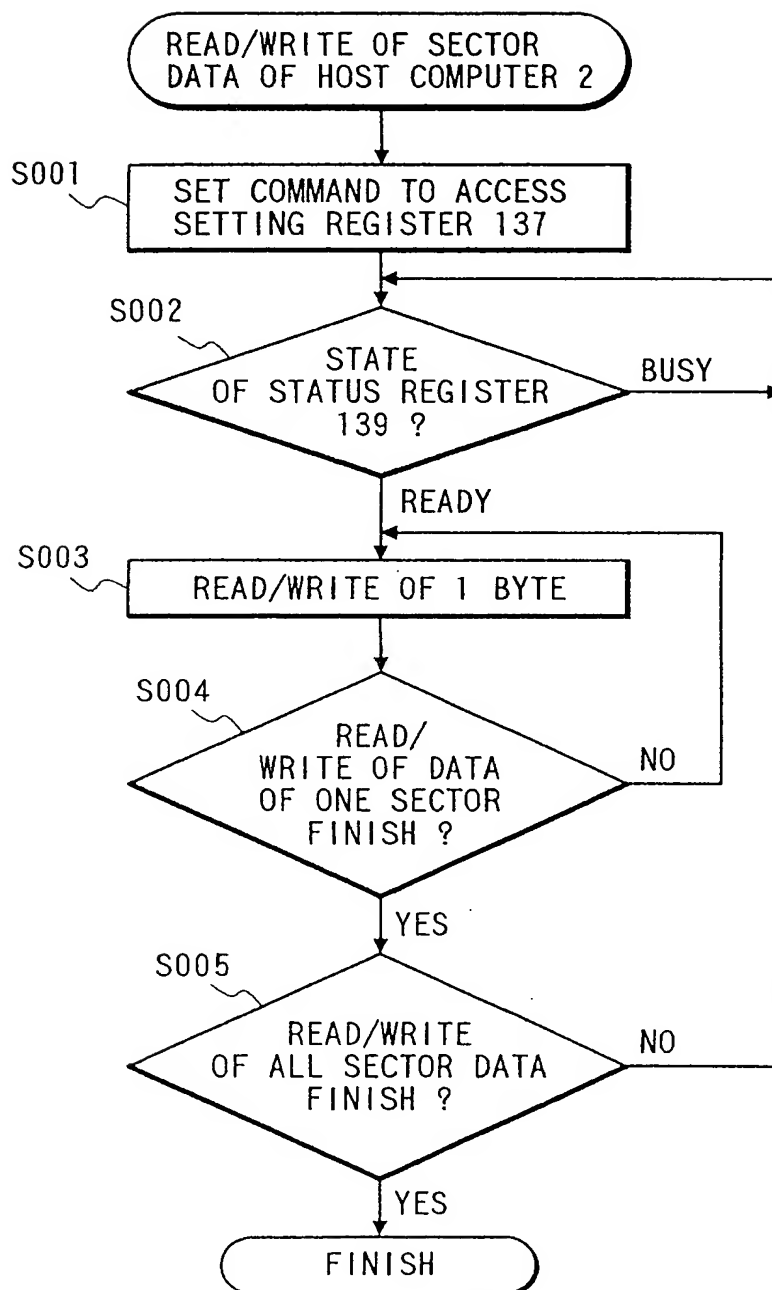


FIG. 7

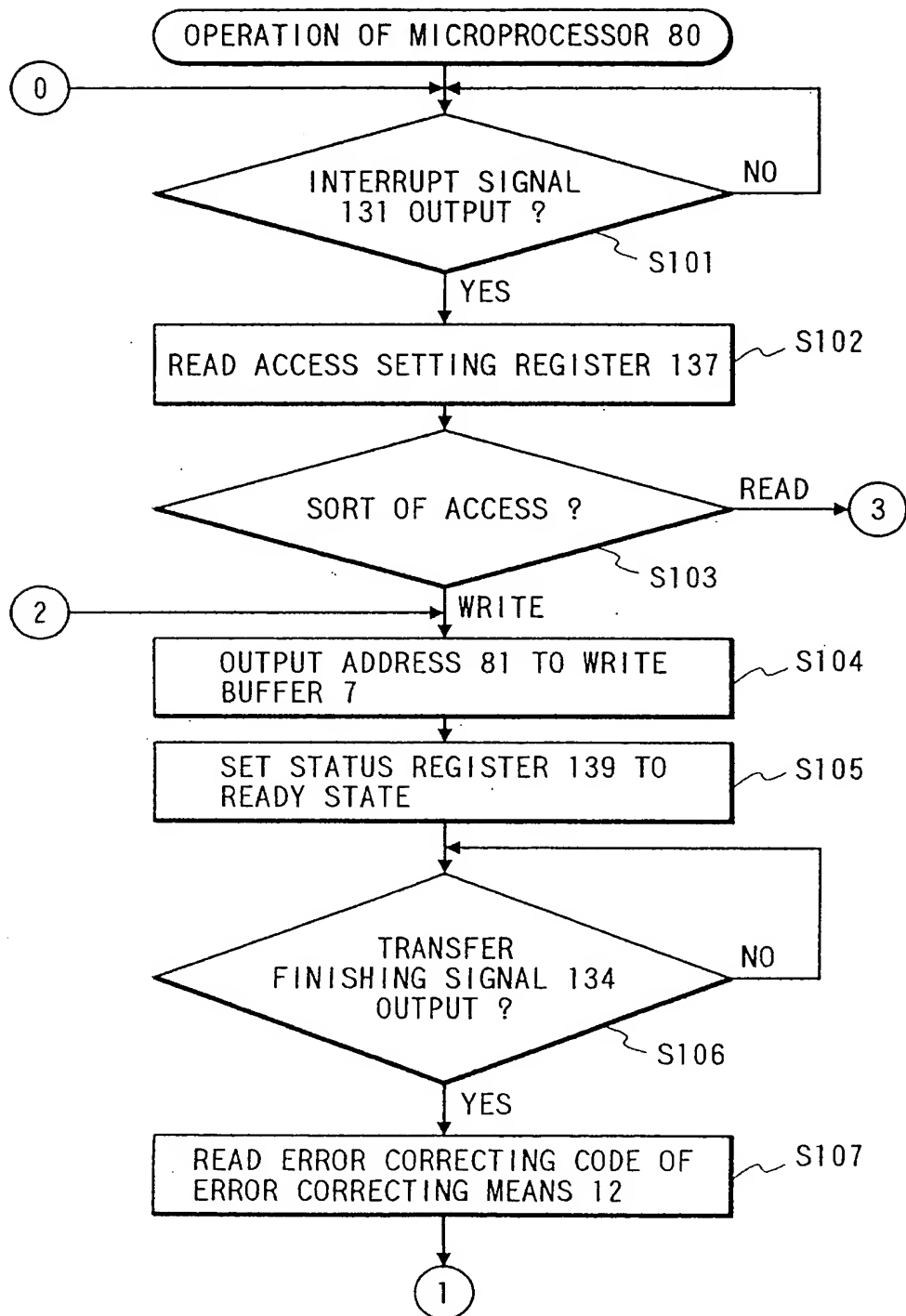


FIG. 8

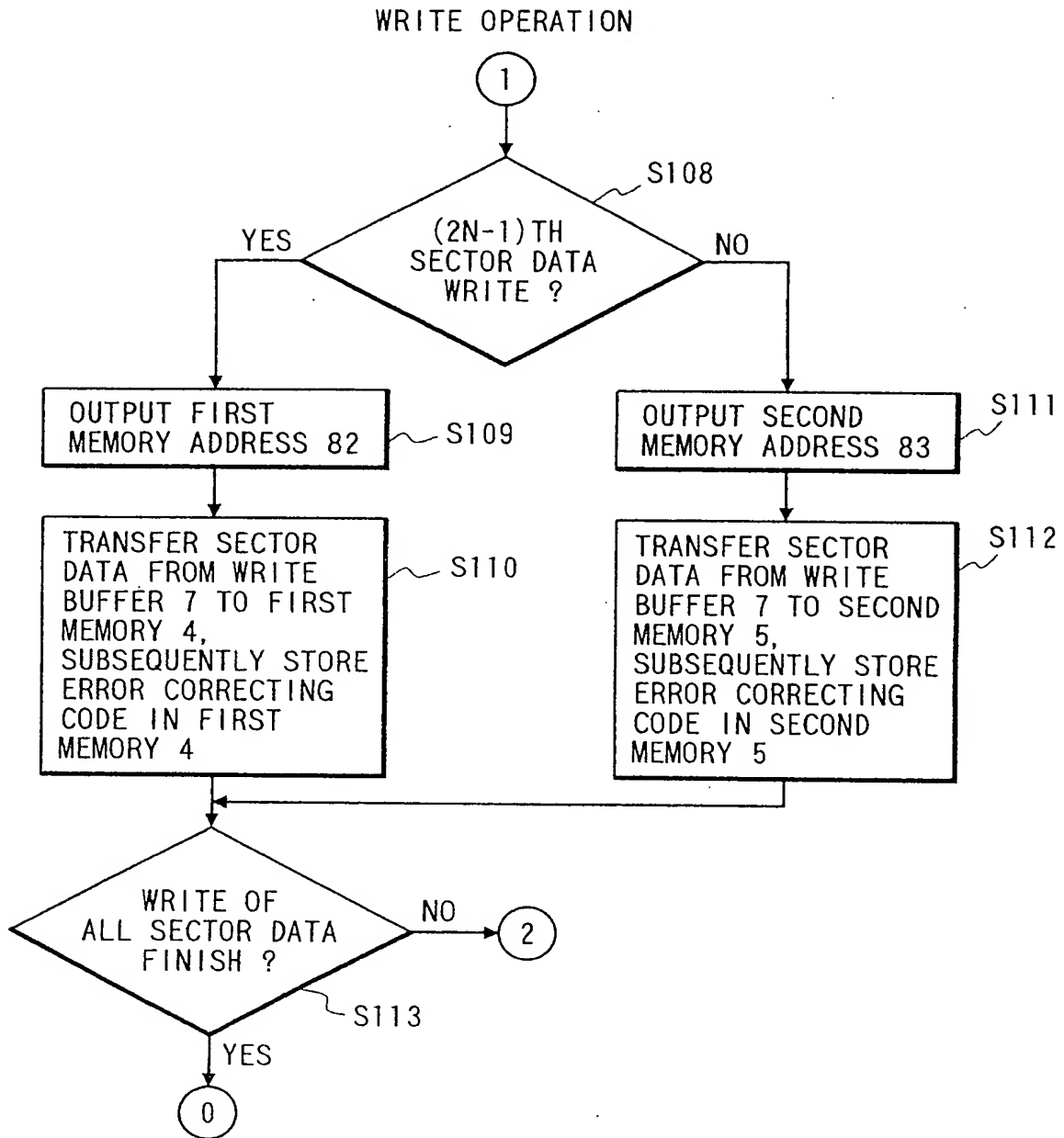


FIG. 9

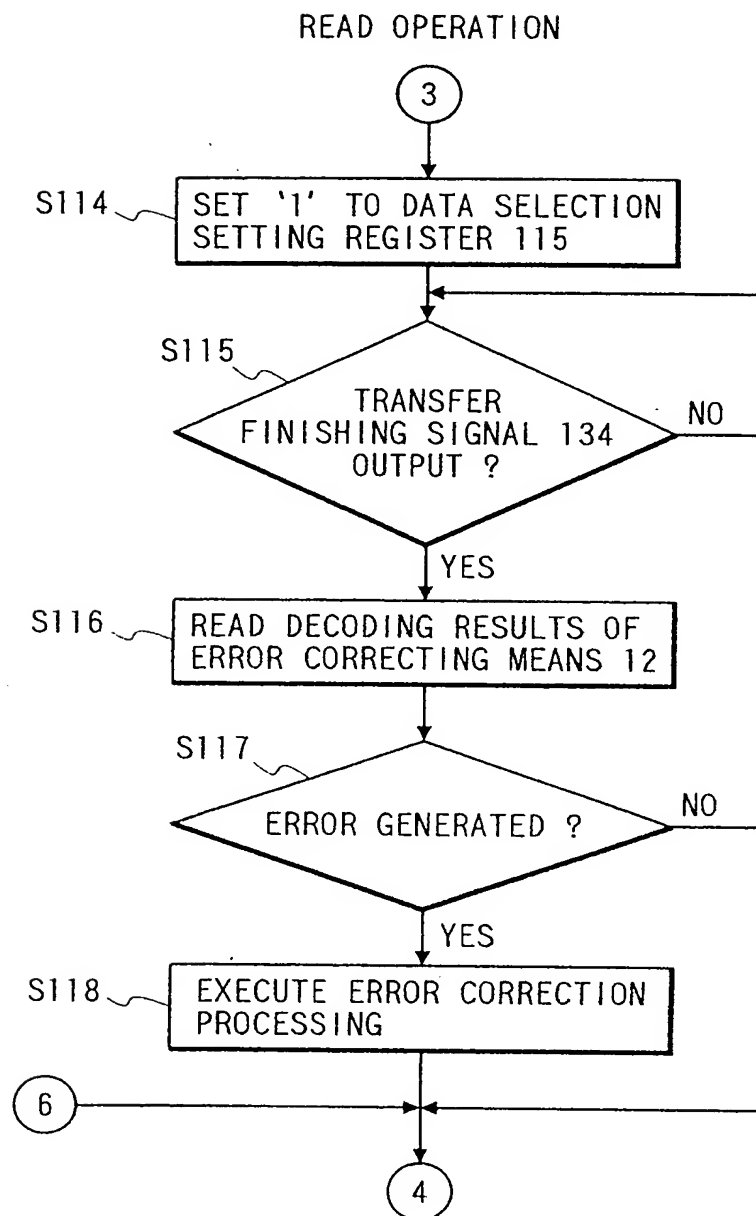


FIG. 10

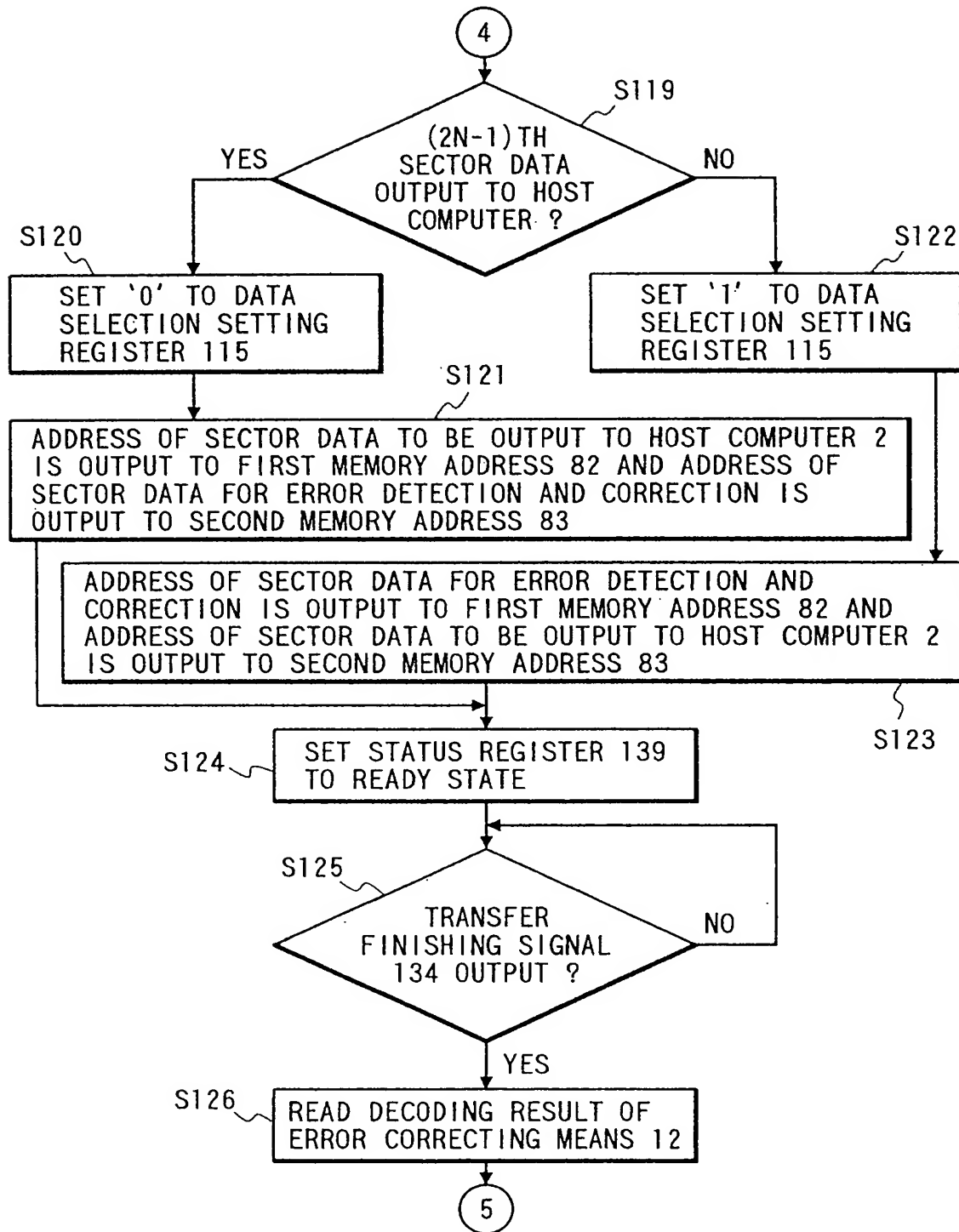


FIG. 11

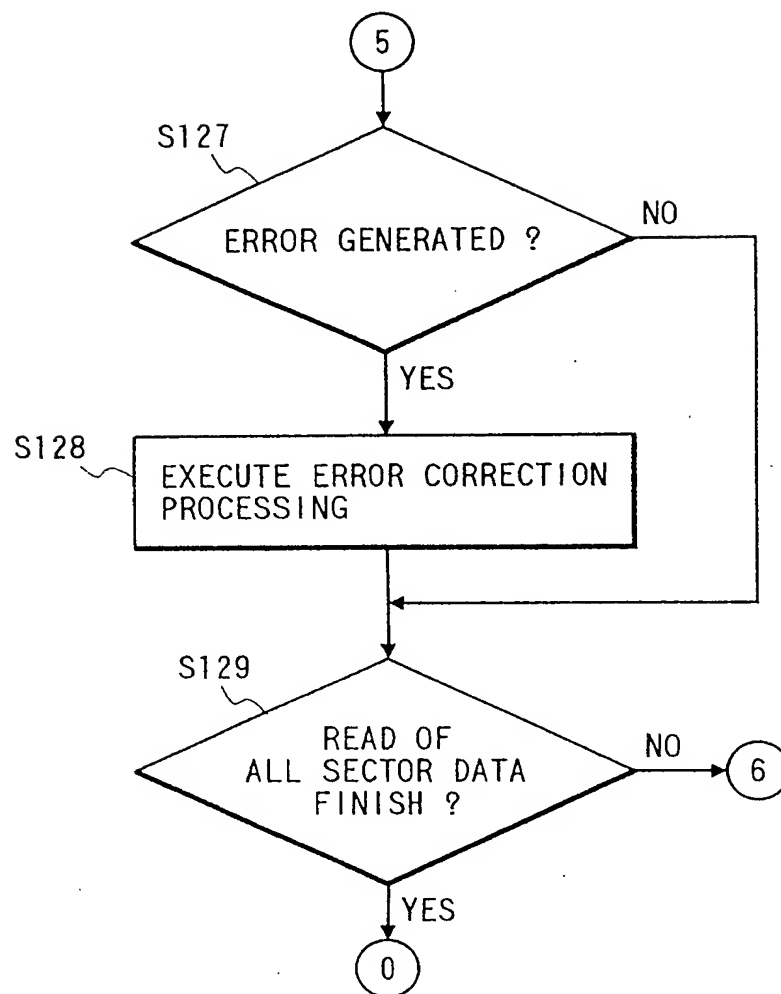


FIG. 12

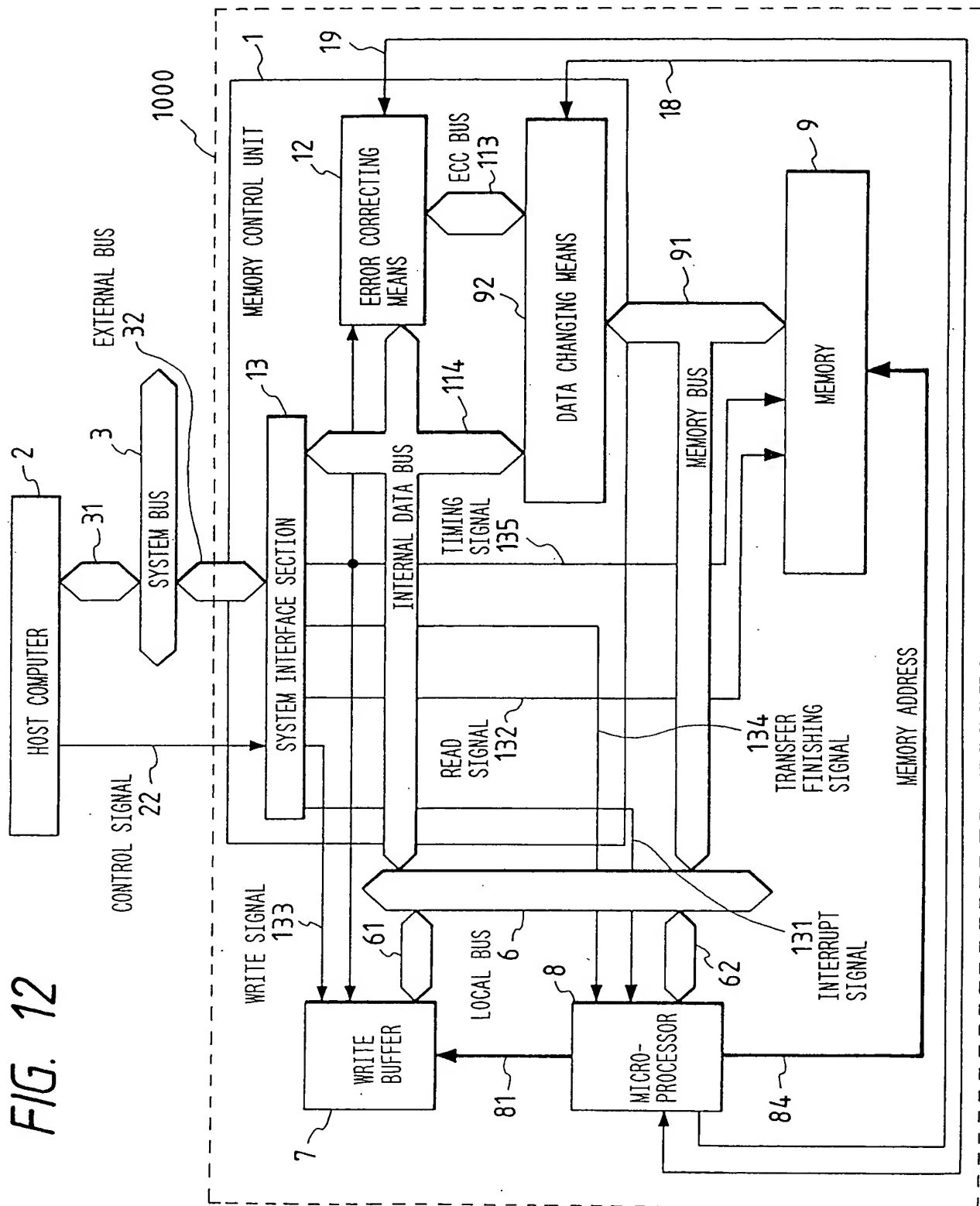


FIG. 13

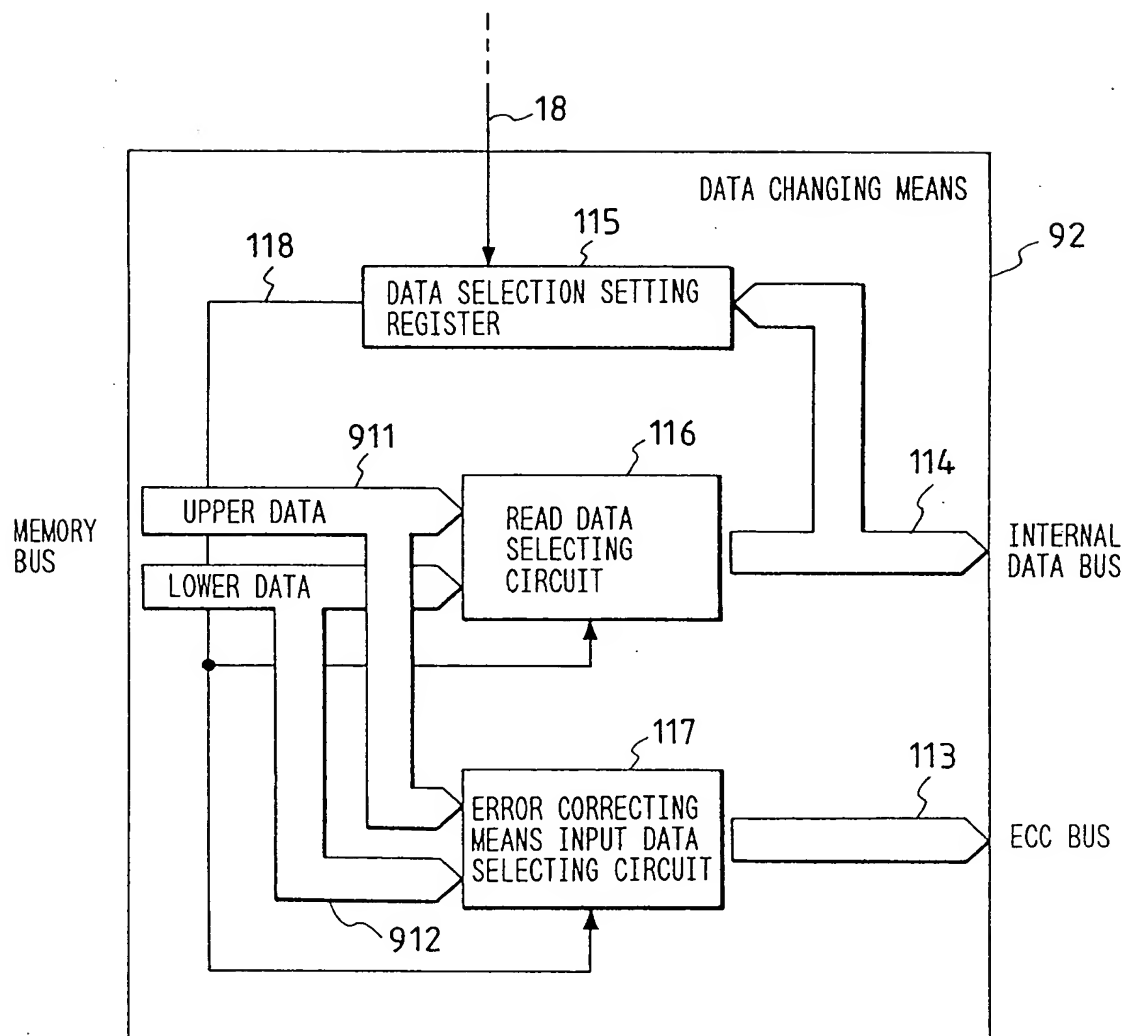


FIG. 14

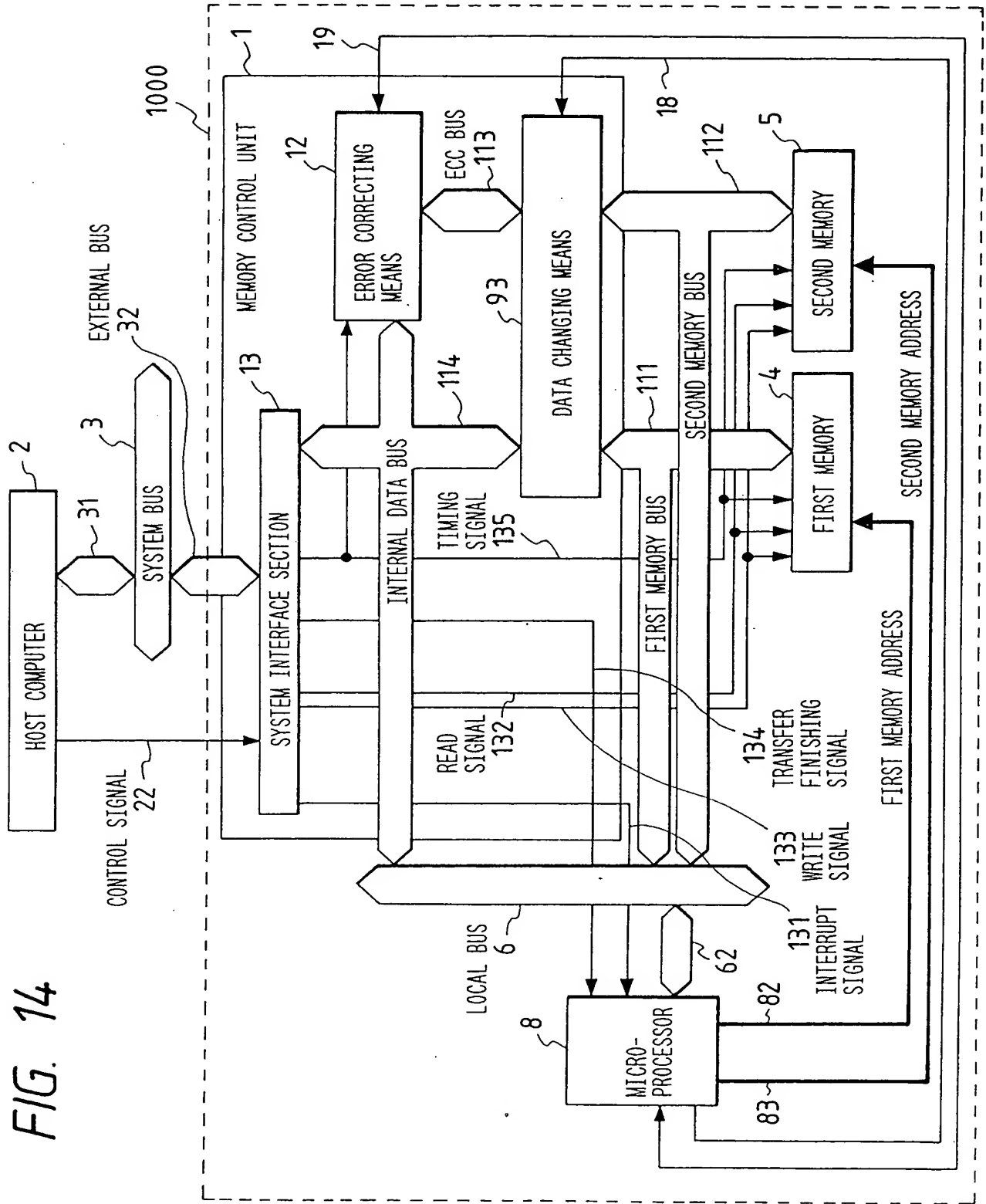


FIG. 15

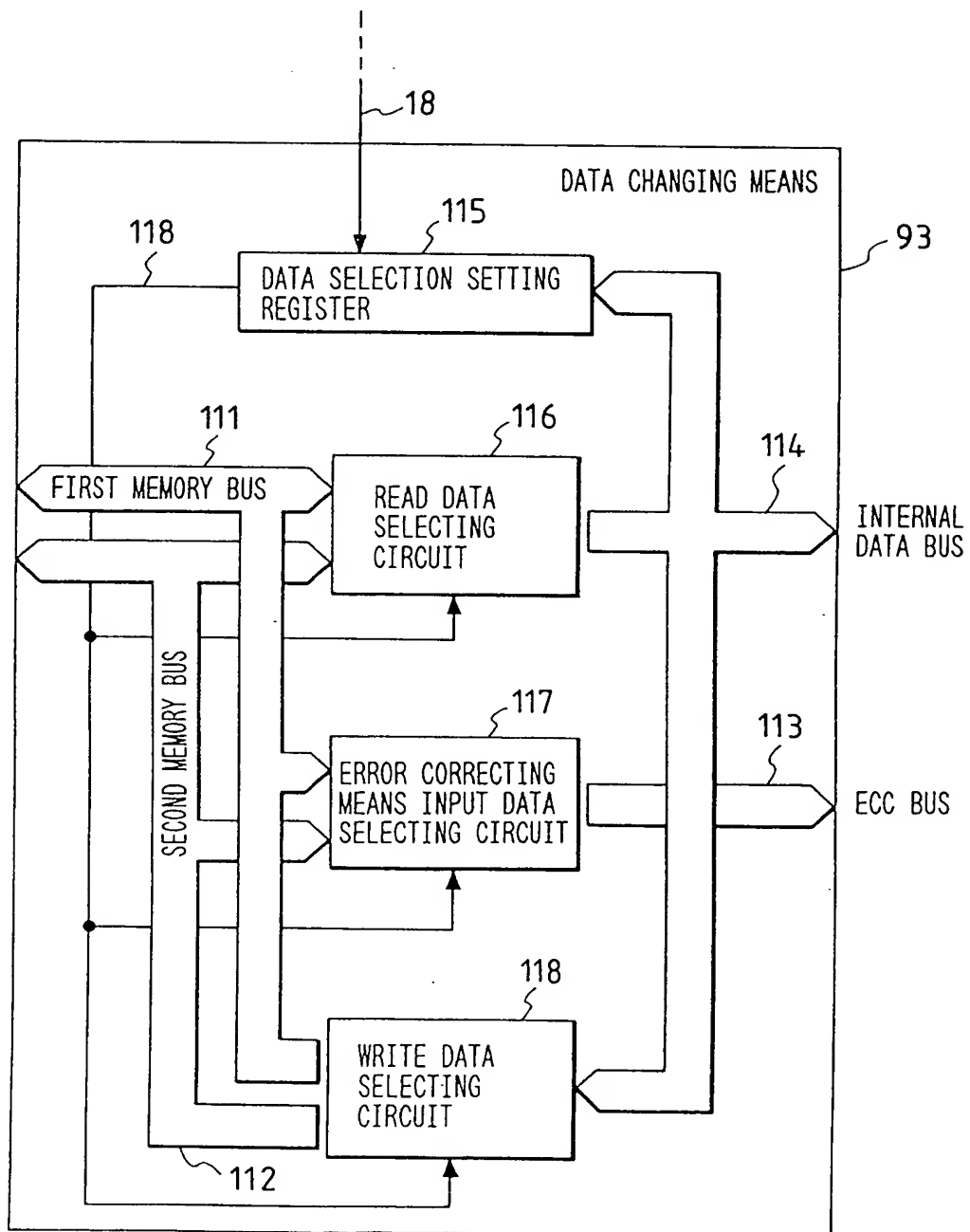


FIG. 16

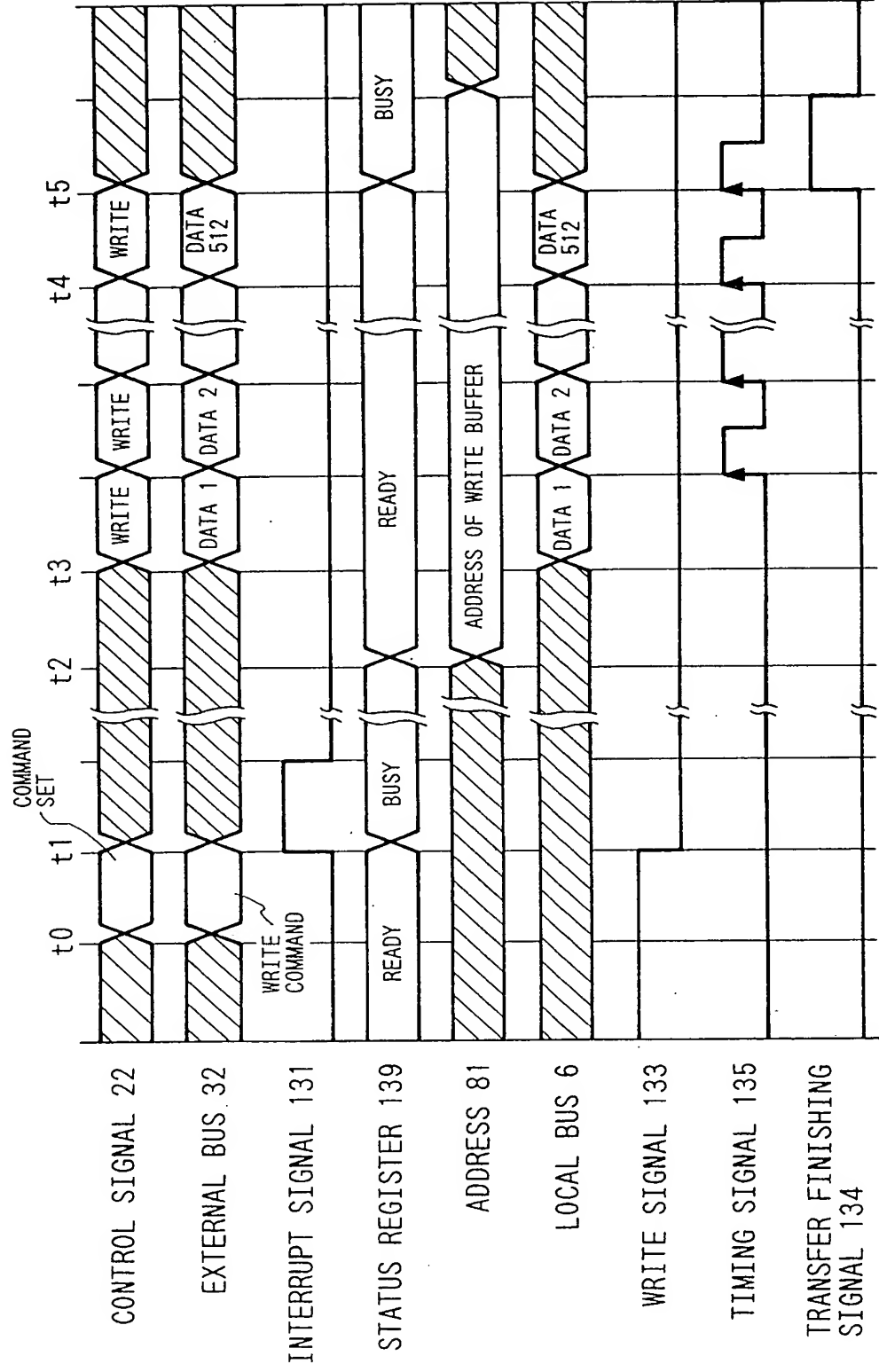


FIG. 17

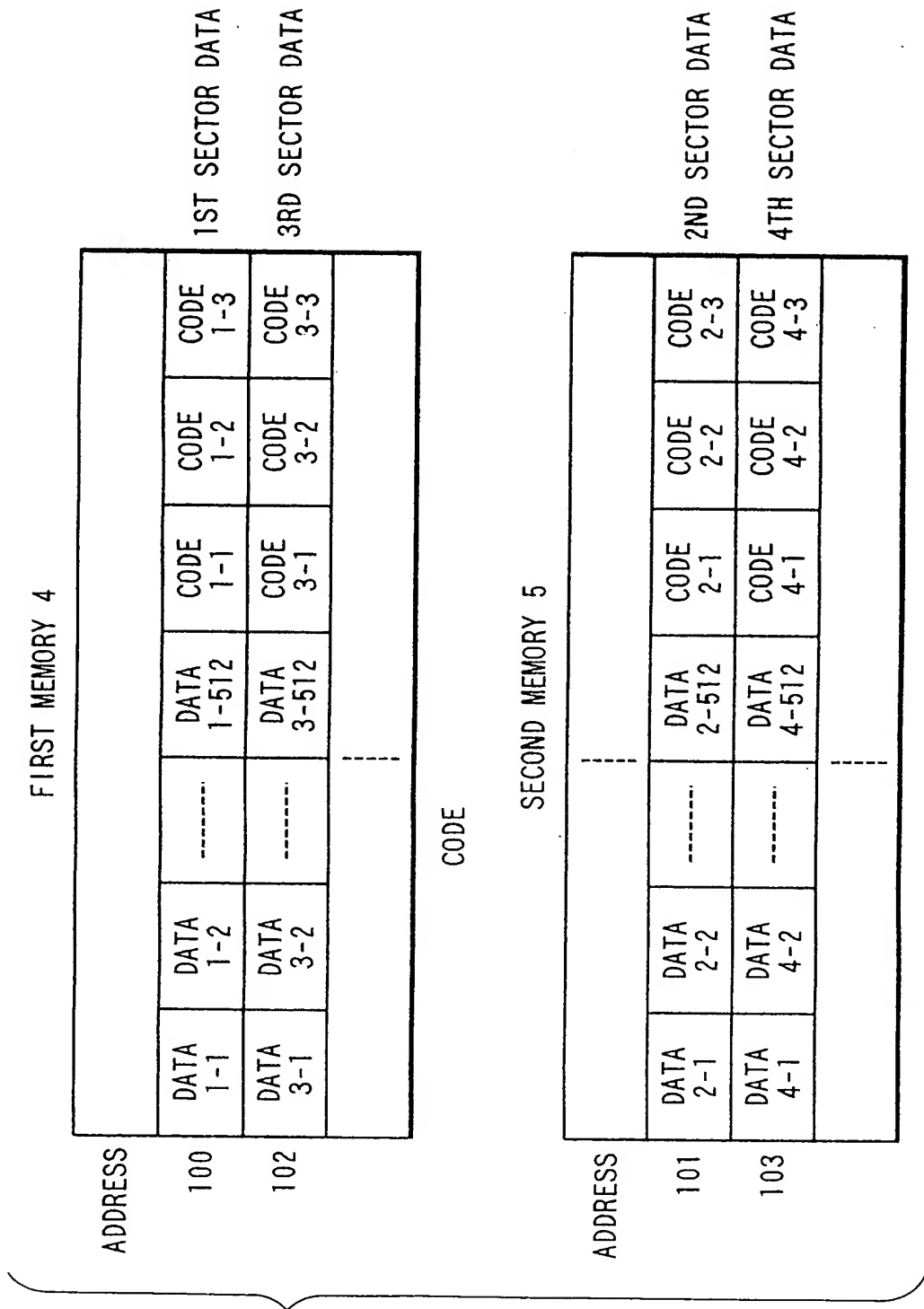


FIG. 18

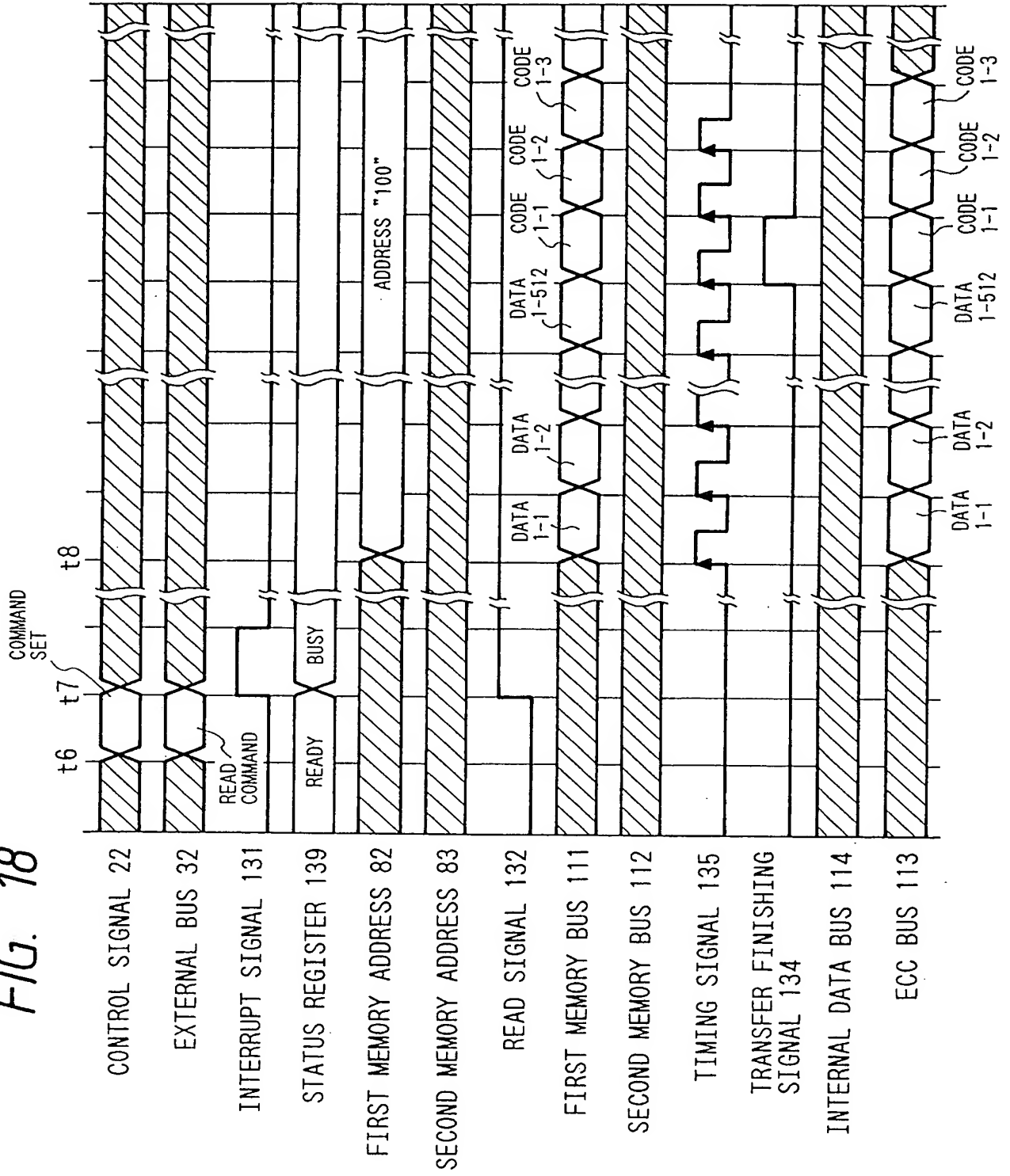


FIG. 19

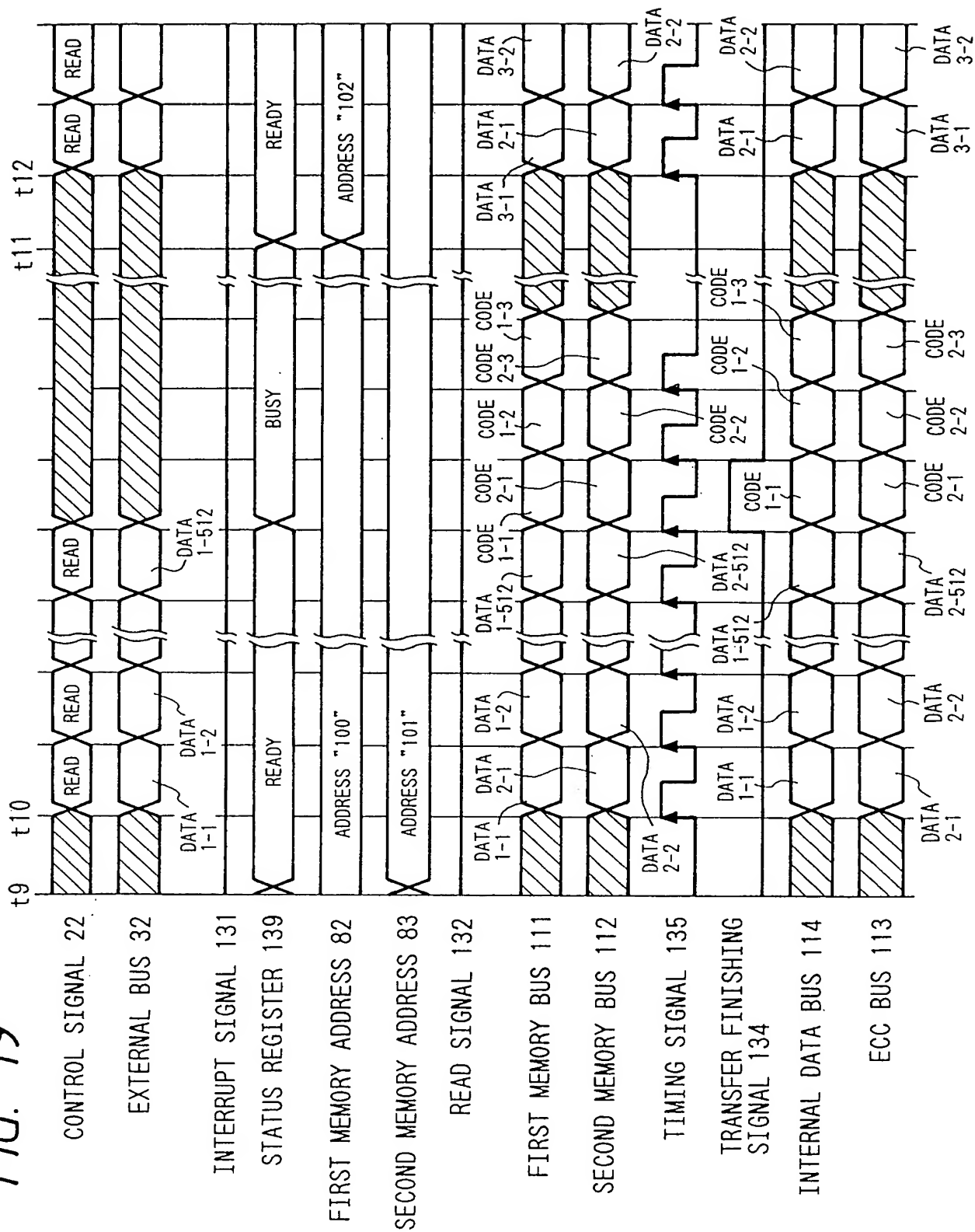


FIG. 20

